

# HOFSTETTER

PCB PLATING



APL  
HOFSTETTER  
PCB GmbH

Overview  
final finishes  
Hofstetter Group

# Overview

## final finishes

### Hofstetter Group

Process	Name	Service location	Supplier	Available on demand	Type of equipment
<b>ENIG</b> <sup>(4)</sup>	electroless Nickel/Gold	Herrenberg and Küssnacht	Uyemura/Umicore	✓ (Herrenberg)	vertical
<b>ENIG TRG</b> <sup>(4)</sup>	electroless Nickel with semi-reductive Gold	Küssnacht	Uyemura/Umicore	✓ (Herrenberg)	vertical
<b>ENEPIG</b> <sup>(4)</sup>	electroless Nickel/Palladium with semi-reductive Gold	Küssnacht	Uyemura/Umicore	✓ (Herrenberg)	vertical
<b>EPIG</b> <sup>(4)</sup>	electroless Palladium with semi-reductive Gold	Küssnacht	Uyemura/Umicore	✓ (Herrenberg)	vertical
<b>DIG</b> <sup>(4)</sup>	direct immersion Gold (semi-reductive)	Küssnacht	Uyemura/Umicore	✓ (Herrenberg)	vertical
<b>immersion Sn</b>	immersion Tin	Lörrach and Herrenberg	Atotech	✓ (Lörrach)	horizontal
<b>immersion Ag</b>	immersion Silver	Herrenberg and Küssnacht	MacDermid Enthone	✓ (Herrenberg)	horizontal
<b>OSP HT</b>	OSP High Temperatur	Herrenberg	Shikoku/Hillebrand	✓ (Herrenberg)	horizontal
<b>elyt. Sn</b>	electroplated Tin	Küssnacht	Schlötter	✓ (Herrenberg)	vertical
<b>elyt. Sn/Pb</b>	electroplated Tin/Lead	Küssnacht	DuPont	✓ (Herrenberg)	vertical
<b>elyt. Ni/Pure Au</b>	electroplated Nickel/Soft Gold	Küssnacht	DuPont	✓ (Herrenberg)	vertical
<b>elyt. Ni/Au Hard</b>	electroplated Nickel/Hard Gold	Herrenberg and Küssnacht	DuPont/Enthone	✓ (Herrenberg)	vertical
<b>elyt. Pure Au</b>	electroplated Direct Gold	Küssnacht	DuPont	✓ (Herrenberg)	vertical

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Process	Panel thickness min. to max. <sup>(1)</sup>	Panel size min. to max. <sup>(2)</sup>	Aspect ratio BV	Aspect ratio PTH	Shelf Life <sup>(5)</sup>
ENIG <sup>(4)</sup>	0.012 mm to 16 mm	100 × 100 mm to 610 × 610 mm	1 : 1	1 : 16	12 Months
ENIG TRG <sup>(4)</sup>	0.012 mm to 16 mm	100 × 100 mm to 610 × 610 mm	1 : 1	1 : 16	12 Months
ENEPIG <sup>(4)</sup>	0.012 mm to 16 mm	100 × 100 mm to 610 × 610 mm	1 : 1	1 : 16	12 Months
EPIG <sup>(4)</sup>	0.012 mm to 16 mm	100 × 100 mm to 610 × 610 mm	1 : 1	1 : 16	12 Months
DIG <sup>(4)</sup>	0.012 mm to 16 mm	100 × 100 mm to 610 × 610 mm	1 : 1	1 : 16	3 – 6 Months <sup>(12)</sup>
immersion Sn	0.012 mm to 5 mm	100 × 60 mm to 610 × 2000 mm <sup>(3)</sup>	1 : 1	1 : 16	6 Months
immersion Ag	0.012 mm to 6 mm	100 × 60 mm to 610 × 2000 mm <sup>(3)</sup>	1 : 1	1 : 16	12 Months
OSP HT	0.012 mm to 5 mm	100 × 60 mm to 610 × 2000 mm <sup>(3)</sup>	1 : 1	1 : 16	6 Months
elyt. Sn	0.012 mm to 20 mm	100 × 60 mm to 610 × 1800 mm	1 : 1	1 : 16	12 Months
elyt. Sn/Pb	0.012 mm to 20 mm	100 × 60 mm to 610 × 1800 mm	1 : 1	1 : 16	12 Months
elyt. Ni/Pure Au	0.012 mm to 20 mm	100 × 60 mm to 610 × 1100 mm	1 : 1	1 : 12	12 Months
elyt. Ni/Au Hard	0.012 mm to 20 mm	100 × 60 mm to 610 × 1100 mm	1 : 1	1 : 12	12 Months
elyt. Pure Au	0.012 mm to 20 mm	100 × 60 mm to 610 × 1100 mm	1 : 1	1 : 12	12 Months

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Process	Layer 1	Thickness 1	Layer 2	Thickness 2	Layer 3	Thickness 3
ENIG <sup>(4)</sup>	Nickel	3 – 7 µm <sup>(6)</sup>	Gold	0.05 – 0.1 µm	n.a.	
ENIG TRG <sup>(4)</sup>	Nickel	3 – 7 µm <sup>(6)</sup>	Gold	0.05 – 0.1 µm <sup>(7)</sup>	n.a.	
ENEPIG <sup>(4)</sup>	Nickel	3 – 7 µm <sup>(6)</sup>	Palladium	0.08 – 0.3 µm <sup>(8)</sup>	Gold	0.03 – 0.08 µm <sup>(7)</sup>
EPIG <sup>(4)</sup>	Palladium	0.1 – 0.2 µm <sup>(9)</sup>	Gold	0.1 – 0.2 µm <sup>(7)</sup>	n.a.	
DIG <sup>(4)</sup>	Gold	0.2 – 0.3 µm <sup>(10)</sup>	n.a.		n.a.	
immersion Sn	Tin	0.6 – 1.2 µm <sup>(11)</sup>	n.a.		n.a.	
immersion Ag	Silver	0.15 – 0.4 µm	n.a.		n.a.	
OSP HT	OSP	0.15 – 0.3 µm	n.a.		n.a.	
elyt. Sn	Tin	5 – 15 µm	n.a.		n.a.	
elyt. Sn/Pb	Tin/Lead	5 – 15 µm	n.a.		n.a.	
elyt. Ni/Pure Au	Nickel	3 – 10 µm	Gold	0.1 – 10 µm	n.a.	
elyt. Ni/Au Hard	Nickel	3 – 8 µm	Gold	0.2 – 10 µm	n.a.	
elyt. Pure Au	Gold	> 3 µm	n.a.		n.a.	



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### Classification

- (++) excellent
- (+) good
- (0) with restrictions
- (-) poor
- (--) not recommended

Suitable for:

Process	Lead-free solder	Leaded solder	Gold wire bonding	Aluminum wire bonding	Press-fit	Connectors	High frequency application	Fineline (< 30 µm)	Button connectors	Corrosion resistance	Reworkability	Storage + Packaging
ENIG <sup>(4)</sup>	(+)	(+)	(-)	(+)	0	0	(--)	(-)	0	0	(--)	(+)
ENIG TRG <sup>(4)</sup>	(++)	(++)	0	(--)	0	0	(--)	(-)	0	0	(--)	(+)
ENEPIG <sup>(4)</sup>	(++)	(-)	(++)	(++)	0	(+)	(--)	(-)	(+)	(+)	(--)	(++)
EPIG <sup>(4)</sup>	(++)	(+)	(+)	(+)	0	(-)	(+)	0	0	0	(--)	0
DIG <sup>(4)</sup>	(++)	(++)	(+)	(-)	0	(--)	(++)	(++)	(--)	(--)	(+)	(-)
immersion Sn	(+)	(+)	(--)	(--)	(++)	(--)	(+)	0	(--)	0	(++)	(-)
immersion Ag	(+) <sup>(14)</sup>	(+) <sup>(14)</sup>	(--)	(--)	0	(--)	(++)	0	(--)	(-)	0	(-)
OSP HT	(+) <sup>(14)</sup>	(+) <sup>(14)</sup>	(--)	(--)	(--)	(--)	(+)	(+)	(--)	(--)	(++)	(-)
elyt. Sn	(++)	(++)	(--)	(--)	(++)	(-)	(0)	(-)	(-)	(+)	(+)	(+)
elyt. Sn / Pb	(++)	(++)	(--)	(--)	(++)	(-)	(0)	(-)	(-)	(+)	(+)	(+)
elyt. Ni/Pure Au	(+)	(+)	(++)	(+)	0	(+)	(--)	(-)	(+)	(++)	(-)	(++)
elyt. Ni/Au Hard	(--)	(--)	(--)	(--)	0	(++)	(--)	(-)	(++)	(++)	(-)	(++)
elyt. Pure Au	(+)	(+)	(+)	0	0	0	(+)	0	(+)	(+)	(+)	0

# An extensive range of unleaded surfaces finishes for complex technical requirements.

## Legend

- 1) Other panel thicknesses on request.
- 2) Other panel thicknesses on request.
- 3) Reel to Reel possible.
- 4) Additional pre-treatment may be necessary after consultation, depending on base material i.e. (tempering in Herrenberg and Küssnacht possible) (plasma, jet pumice, permanganate, tin stripping in Küssnacht possible).
- 5) Other layer thicknesses can lead to changes. Important: Correct packaging and storage conditions (please note specifications).
- 6) 1.5  $\mu\text{m}$  – 10  $\mu\text{m}$  possible (Upper range min. 2  $\mu\text{m}$ ).
- 7) Layer thicknesses up to max. 0.2 – 0.3  $\mu\text{m}$  possible with semi-reductive gold bath (range over 0.1  $\mu\text{m}$  layer thickness, always minimum 0.1  $\mu\text{m}$  deviation).
- 8) Layer thicknesses greater than 0.3  $\mu\text{m}$  possible, but no advantage.
- 9) Higher layer thicknesses possible, above 0.4  $\mu\text{m}$  is not recommended.
- 10) Maximum layer thickness 0.2 – 0.3  $\mu\text{m}$ , lower layer thicknesses possible for pure soldering applications and after testing, minimum layer thickness 0.1 – 0.2  $\mu\text{m}$ . Depending on the pattern and material, the min/max value must be adjusted, because the variations are bigger. This assessment can only be made after the plating.
- 11) Maximum layer thickness 1.2  $\mu\text{m}$  (1.2  $\mu\text{m}$  only possible with two runs)
- 12) Depending on the application.

- 13) Different materials behave differently. It is not always possible to make a general statement. We recommend asking and or even better, having material tested with a relevant pattern design.
- 14) From 2 $\times$  reflow steps, various influences can have a negative effect (e.g. a long waiting time between individual reflow steps).

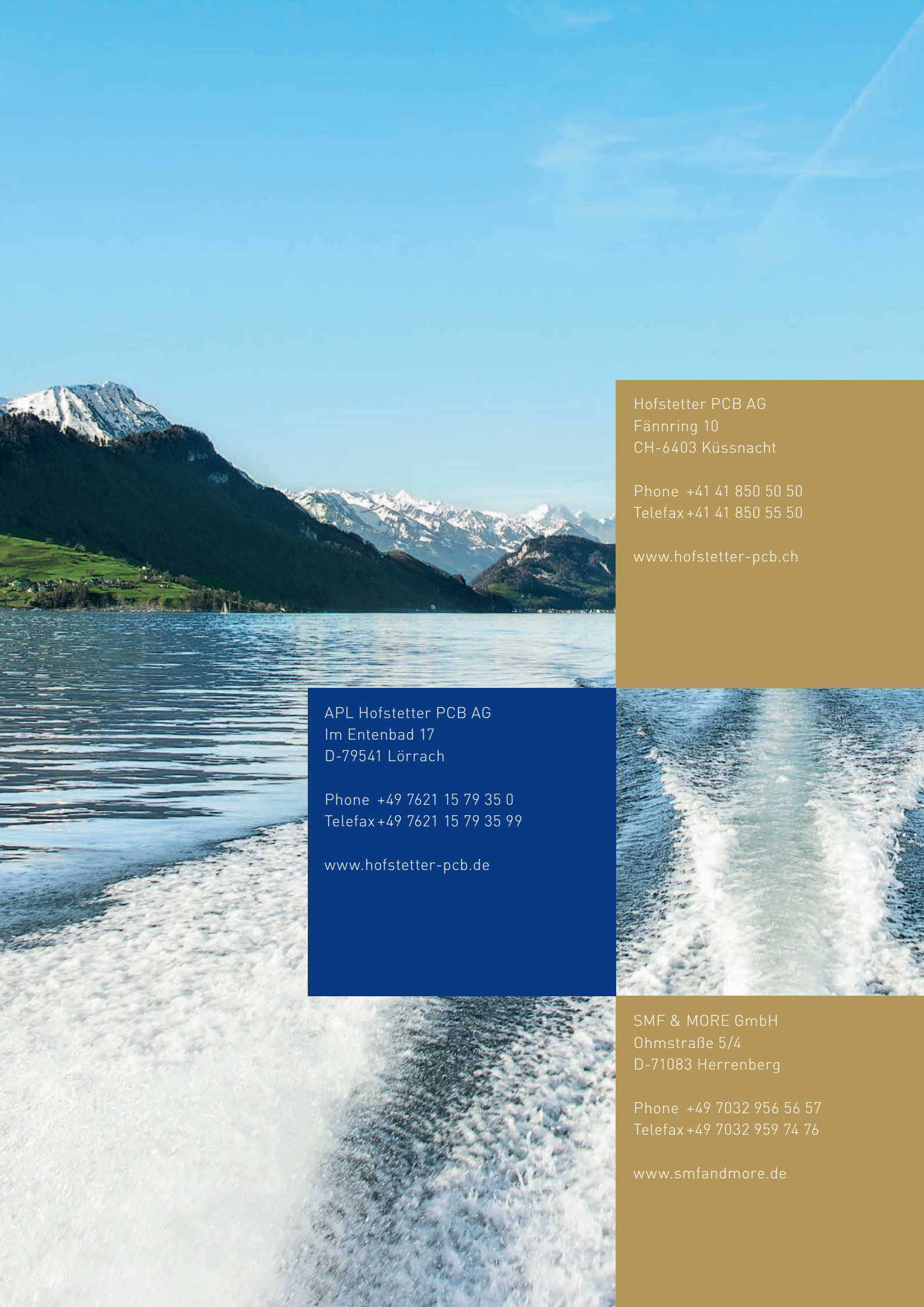
## General delivery condition:

- A) Soldermask as well as electrolytic gold resists must be completely polymerized (resists are not allowed in electroless final finishes). Use of peelable soldermask is allowed.
- B) Through holes MUST be completely open or completely closed. Otherwise no guarantee can be given for a successful plating, especially in electroless processes.
- C) Copper must be free of any contamination, residues of soldermask or resist prevent plating at the affected areas and/or cause problems with the plating (pretreatment is possible after consultation).
- D) Maximum aspect ratio 1:1 of blind vias in the plated condition.
- E) Panels from stock have to be desoxidized and dry before sending to us.
- F) LDI and DI masks have to be additionally UV bump cured.

- G) In electrolytic processes, all surfaces to be plated must be connected with sufficient electric conductor cross-section (contact for galvanic Sn and SnPb over long edge, for galvanic Ni/Au over short edge possible).
- H) General requirements to the immersion tin process are available at <http://www.apl-ssc.com/kontakt/downloads/>

## Information on delivery note and on request for quotation

- A) Ordered final finish with process name e.g. ENEPIG or galv. Ni/Bondgold etc.
- B) Layer thickness of each layer with range e.g. nickel 3 – 7  $\mu\text{m}$  /palladium 0.08 – 0.20  $\mu\text{m}$  /gold 0.03 – 0.08  $\mu\text{m}$  (mean value is the target value) or indication of the minimum layer thickness (mostly galv. gold) nickel min. 4  $\mu\text{m}$  /gold min. 1.0  $\mu\text{m}$ .
- C) Indication of the complete active surface area incl. panel frame and surface of the holes, especially for processes where precious metals such as palladium and gold are required.
- D) Length, width and material thickness of the panel
- E) Blind holes yes/no, if yes which aspect ratio.
- F) Requested delivery date at leaving Hofstetter site.
- G) Specification of outer layer material (especially for final finishes such as ENIG/ENIG TRG/ENEPIG/EPIG/ISIG/DIG).
- H) Specification of solder mask type requested.



Hofstetter PCB AG  
Fännring 10  
CH-6403 Küssnacht

Phone +41 41 850 50 50  
Telefax +41 41 850 55 50

[www.hofstetter-pcb.ch](http://www.hofstetter-pcb.ch)

APL Hofstetter PCB AG  
Im Entenbad 17  
D-79541 Lörrach

Phone +49 7621 15 79 35 0  
Telefax +49 7621 15 79 35 99

[www.hofstetter-pcb.de](http://www.hofstetter-pcb.de)

SMF & MORE GmbH  
Ohmstraße 5/4  
D-71083 Herrenberg

Phone +49 7032 956 56 57  
Telefax +49 7032 959 74 76

[www.smfandmore.de](http://www.smfandmore.de)